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PATENT 01/25/02
#4
5-102

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of

Jayaraman, et al.

Serial No.: 09/921,513

Filed: August 1, 2001

For: Method and Apparatus for
Adjusting Delay in Systems
With Time-Burst Pilot and
Fractionally Spaced Equalizers)

Examiner: Unknown

Group Art Unit: 2661

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LETTER TO OFFICIAL DRAFTSMAN
TRANSMITTING FORMAL DRAWINGS

Asst. Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

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MAR 27 2002

Technology Center 2600

Enclosed for filing in the subject application are six (6) sheets of formal drawings.

Respectfully submitted,

Dated: 3 January 2002

By: S. Hossain Beladi

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231, on:

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